

- CVD64 Ver 1.52 (2021/05) Release Note

Division	Items	Contents
New	UI	Support Cortex-A76/A77/A78
	UI	Support ARM-v9 architecture
	UI	Support [FreeRTOS] os awareness
	UI	Add [Register]-[User Register] menu
Fix	UI	Fixed ranged watchpoint can't show correctly issue.
	UI	If can't set ranged BP/WP, then it will show warning message
	UI	Fixed can't set corebase/citbase when address is bigger than 32bit
	UI	Fixed can't show correct callstack issue
	UI	Fixed disassembly error on 0xD2C00020 issue
	UI	Fixed data.test error on ARMv9
	API	Fixed CVD_ReadWord64() get wrong value issue.
	Firmware	Fixed jump to 0x0 on warm reset when using multicore issue.
	Firmware	Fixed setting wrong cti base address on SWD mode issue
	Firmware	Fixed ELR value changing issue on ARMv9

- CVD64 Ver 1.43 (2020/06) Release Note

Division	Items	Contents
New	CMD	Support IF (&var==100)&&(&NUM==(0x400 0x800 0x1000)) style in script file
	CMD	Support NewLine command in Per file
	CMD	Add OPTION.SecureDebugMode on/off for secure/non-secure debugging
	CMD	Support /Censorchip option for FLASH.Auto command
Fix	CMD	Fixed data.set not working when using flash programing
	UI	Fixed bit field crash issue
	UI	Fixed callstack list with GreenHills compiler
	UI	Fixed status bar output error message twice if firmware is not match
	UI	Fixed can't connect to target board if MemoryAccessPort set to 2 in Cortex-M
	UI	Fixed MMU list issue with nGnRE attrfield value

CVD64 ver 1.42(2019/03) Release Note

NEW	UI	Support Cortex-A55/A75/A76
	UI	CVD-API can be used on multi-Codeviser
	UI	Support Cortex-R52
	UI	Support Linux kernel 4.09~ os awareness
	UI	Support Icache/Dcache dump(Cortex-A53/A55/A57/A72/A73/A75)
	UI	Break list window can also save as cmm file
	UI	If can't install software breakpoint, then CVD will install onchip breakpoint
	UI	Change General Register window
	UI	Add [Copy Symbol]/[copy Address] function in watch window
	CMD	Support DAP:/EDAP: memory class
	CMD	Add SCRIPT.MESSAGE.OPEN/SCRIPT.MESSAGE.CLOSE/SCRIPT.MESSAGE.ERROR/OPEN/SCRIPT.MESSAGE.ErrorCLOSE csf command
	CMD	Support Var.if, Var.Assign,
	CMD	Support SYStem.Option DualPort on/off
	CMD	Support Setup.Dump /Be command
	CMD	Support PRIVATE,PARAMETERS, On error continue,
	CMD	Support %open %recursive option for var.view/var.watch/var.addwatch
	CMD	Support FORMAT.UnixTime() function
	CMD	Support SYStem.Option AHBPROT command
	CMD	Support color option on MENU.addTool command
	CMD	Support LR D SS IL register name on register.set command
CMD	Support /compare /diff option for Data.Set command	
Fix	CMD	Fixed fpu.set command
	Firmware	Fixed the problem that stop doesn't working correct on IT instruction
	UI	Fixed the problem that doesn't support symbol name in memory dump window
	UI	Fixed the problem that can't show macro value in cmm file

	UI	Fixed the problem that can't show static variable value
	UI	Fixed user tool button size
	UI	Fixed can't read EAHB/EAPB/EAXI memory data from .ars file issue
	UI	Fixed the problem that can't decode ERET instruction
	UI	Fixed the problem can't show some static variable on watch window
	CMD	Fixed the problem STRING.SPLIT() doesn't work issue
	CMD	Fixed can't download memory data in sys.m.prepare state
	CMD	Fixed the issue sys.m.prepare doesn't work on after sys.m.a command

CVD64 ver 1.40(2017/06) Release Note

NEW	UI	Support Cortex-A73, ANANKE, ARTEMIS
	UI	Support Threadx os awareness
	CMD	Add OPTION.DisMode aarch64
	CMD	Add SYStem.CONFIG.CTL.CONFIG ARMv8V2
	CMD	Add new command OPTION.DISPLAY AARCH64
Fix	UI	Fixed show CORE.ASSIGN doesn't work issue.
	UI	OS.SCREEN command will not popup does window anymore
	UI	Fixed CVD crash issue on opening callstack window
	UI	Fixed HLT behavior after stop at HLT instruction
	UI	Fixed can't connect to target right after changing core
	UI	Fixed can't load some module symbol issue
	UI	Fixed can't linux app/module/library symbol loading speed issue
	UI	Fixed semihosting doesn't work on AARCH32 user mode
	UI	Fixed can't show big endian symbol issue

CVD64 ver 1.30(2016/06) Release Note

NEW	UI	Support Cortex-A32, Cortex-A35, Cortex-A72
	UI	Support up to 16 cores in one CVD64 UI
	CMD	Add DEBUG.ClearCache command
	CMD	Add JTAG SCAN related commands JTAGSCAN, JTAGIR, JTAGDR
	CMD	Add overlay related commands, OVERLAY.AutoID, OVERLAY.Create OVERLAY.DETECT, OVERLAY.FRIEND, OVERLAY.List OVERLAY.RESet, OPTION OVERLAY
	CMD	Add new command OPTION.DISPLAY ARCH64
Fix	Firmware	Fixed show secure-memory when in non-secure mode issue.
	Firmware	Fixed can't stop at breakpoint after attach
	Firmware	Fixed waitreset option doesn't working issue
	UI	Fixed can't use cvd master/slave issue
	UI	Fixed can't load linux module symbol
	UI	Fixed can't show local variable after relocate symbol
	Firmware	Fixed can't show correct value of ESR_EL3 issue
	UI	Fixed only show 32bit address on linux module code address
	UI	Fixed semihosting doesn't work on AARCH64 mode
	UI	Fixed can't connect to target right after using system.mode prepare command
	UI	Fixed vector catch doesn't show message issue
	UI	Fixed can't correctly disassemble LDRH command issue
	CMD	Fixed Task.lib.address() only return for 32bit address
	CMD	Fixed can't execute term.out command

CVD64 Ver 1.20 (2015/04) Release Note

Change	UI	Automatically change to stopped core on multi cores there are some core didn't stop
Fix	UI	Fixed can't load module symbol issue
	UI	Fixed crash issue on sYmbol.SourcePATH.SetRecurseDir command
	UI	Fixed watch list view can't show 64bit structure

	UI	Fixed can't show 32 bit mmu list bug
	UI	Fixed virtual C source not shown in debug list window issue
	UI	Fixed can't show local/global variable issue
	UI	Fixed ASR , MOVK disassemble bug
	UI	Fixed step out doesn't working on some code issue.
	UI	Fixed variable in watch window becomes '???' issue.
	CMD	Fixed block handling routine on if() command.

- CVD64 Ver 1.10 (2014/12) Release Note

Division	Items	Contents
New	UI	Added OS unlock option in vector catch option view window
	UI	Support ASM callstack
	CMD	Added SYStem.Option SMPMultipleCall OFF/ON command for those target board doesn't support CTI
	Common	SWD support
	Common	Support API
	Common	Semihosting support for AArch32 SVC/USR mode
Change	Common	CTIGATE disabled when connecting single core
	CMD	Support term.method armswi 0x8888
Fix	UI	Fixed high level source did not show
	UI	Fixed memory fill feature
	UI	Fixed store options feature related to vector catch
	UI	Fixed showing status in multi debugging environment
	UI	Fixed the problem when setting software breakpoints
	UI	Fixed loading an image file based on dwarf4 type
	UI	Fixed there was a blurry reflections of local variable view window
	UI	Fixed could not show local variables
	UI	Fixed did not show source files created by DS-5
	UI	Fixed a dump file was made when doing unload a symbol
	UI	Fixed pc would move 0x0 when doing register undo
	Common	Fixed being powered down when using semihosting
	Common	Fixed exception level information was not matched on multi core debugging environment
	Common	Fixed the problem when connecting with CA53
	Common	Fixed stop and run will crash target execution issue
	Common	Fixed the semihosting feature with AARCH32 SVC mode
	CMD	Fixed status check issue on SYStem.Attach command
	CMD	Fixed display type of var.watch
	CMD	Fixed could not connect to 8 cores with SWD mode on CA53
	CMD	Fixed to support vectorcatch.NONSECUREEL1 ON

- CVD64 Ver 1.00 (2014/04) Release Note

Division	Items	Contents
New	Common	Support new core - Cortex-A53/ Cortex-A57 / Cortex-A5753
	Common	Added 64bit Coprocessor register read/write feature
	Common	Support dwarf4 format
	UI	Added Quad as data width for loading or downloading images
	UI	Added ARCH64 as display mode
	UI	Added Quad as data width on break set window
	UI	Added Debug(terminal) status when using semihosting
	CMD	Added the result of data.compare() leads to found()
	CMD	Added data.image /bgr565
Change	Common	Support AXI, EAXI as memory type
	Common	Changed the serial digit numbers
	UI	Deleted CP/Users register windows
	UI	Changed vector catch window

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	UI	Changed Load a symbol image window
	UI	Changed Download Binary/Hex File window
	UI	Deleted show jumping address to vector area option
	UI	Changed TLB registers windows
	UI	Deleted memory access range tab on memory map window
	UI	Changed RTOS customize dialog