

**- CVD\_TriCore Ver 2.31 (2021/06) Release Note**

Division	Items	Contents
New	UI	Support OSEK os awareness
	UI	Support [Speed up] mode
	Firmware	Improve flash downloading speed.
Fix	Firmware	Fixed can't BP can't hit on TC399XP issue
	Firmware	Fixed Run/Stop crash target on TC399XE

**- CVD\_TriCore Ver 2. 21 (2020/06) Release Note**

Division	Items	Contents
New	Common	New Tricore IDs added into core list
	Common	Tricore register xml files included in installation package
	CMD	Support IF (&var==100)&&(&NUM==(0x400  0x800  0x1000)) style in script file
	CMD	Support NewLine command in Per file
	CMD	Support /Censorchip option for FLASH.Auto command
Fix	UI	Multicore run/stop by using hardware method
	UI	Fixed bit field crash issue
	CMD	Fixed data.set not working when using flash programing
	CMD	Core.assign can be used without Core.Number command
	CMD	Fixed flash.create TARGET LONG option error

**- CVD\_TriCore Ver 2. 11 (2019/09) Release Note**

Division	Items	Contents
New	CMD	Support FLASHFILE.TARGET, FlashFile.GETID, FlashFile.LOAD
	CMD	Support space when assign value to a macro. (ex: &a = 0x1000)
	CMD	Support FLASH.SECTOR.SIZE(),FLASH.CHANGEmtype,
	CMD	Support /Censorchip option for FLASH
	CMD	Support SYSystem.Option.Resetbehavior RestoreGo
Fix	CMD	Fixed per file item can't show correct value issue
	CMD	Fixed pbreak not working in step
	CMD	Do without file extension will search .cmm and .csf
	UI	Fixed performance doesn't working on multi slave cvd

**- CVD\_TriCore Ver 2. 01 (2018/05) Release Note**

Division	Items	Contents
New	Common	Support TC390X/390XE core
		Support TC210L core
		Support .sx file downloading
	CMD	Support SYSystem.CONFIG DAP.DAPENable command
Fix	UI	Fixed downloading issue on TC297

**- CVD\_TriCore Ver 1. 10 (2016/04) Release Note**

Division	Items	Contents
New	Common	Support a new feature
		- MCDS Trace FIFO Full
		Support Detach as system mode
	- [Config]-[Interface]	
	CMD	Added MCDS commands
		- CTRace.MCDS.FifoFullAction [Circular   Break   Extended]
		- CTRace.MCDS.FifoBreakLeVel [100   80   60   40   20]
		- CTRace.MCDS.FifoExtendedSize [2   4   8   16   32   64   128   256   512   1024]
	UI	Added a feature related to SFR registers

		- [Register]-[Select SFR Registers] - [Register]-[Special Function Registers] - [Register]-[Special Function Registers with Name]
Fix	UI	Fixed the problem supported only four hardware breakpoints on simulator mode Fixed the number of functions show in stackframe window Fixed the problem did not work source option on stackframe window Fixed the problem changed options after CVD quits Fixed the problem the status on bar and the icon status are different when connect with TC230ED selected Fixed the problem could not load .sw file Fixed the problem showed abnormal source path Fixed the problem did not show local variables Fixed the problem did not stop or break on simulator mode

**- CVD\_TriCore Ver 1.10 (2015/12) Release Note**

Division	Items	Contents
New	Common	Support new features - MCDS Trace
	CMD	Added MCDS commands - CTRace.FILE [filename] - CTRace.list - CTRace.MCDS [Enable   Disable] - CTRace.MCDS.EMEM.Range <address_range> - CTRace.MCDS.EMEM.Tiles [TCM   XTM] - CTRace.MCDS.Init - CTRace.MCDS.RESet - CTRace.MCDS.SPB.ReadAddr [ON   OFF] - CTRace.MCDS.SPB.ReadData [ON   OFF] - CTRace.MCDS.SPB.WriteAddr [ON   OFF] - CTRace.MCDS.SPB.WriteData [ON   OFF] - CTRace.MCDS.SRI1.ReadAddr [ON   OFF] - CTRace.MCDS.SRI1.ReadData [ON   OFF] - CTRace.MCDS.SRI1.WriteAddr [ON   OFF] - CTRace.MCDS.SRI1.WriteData [ON   OFF] - CTRace.MCDS.SRI1.SLAVE [CPU0   CPU1   CPU2   LMU   DF   PF0   PF1   XBAR] - CTRace.MCDS.SRI2.ReadAddr [ON   OFF] - CTRace.MCDS.SRI2.ReadData [ON   OFF] - CTRace.MCDS.SRI2.WriteAddr [ON   OFF] - CTRace.MCDS.SRI2.WriteData [ON   OFF] - CTRace.MCDS.SRI2.SLAVE [CPU0   CPU1   CPU2   LMU   DF   PF0   PF1] - CTRace.MCDS.TCX.CORE [NONE   CPU0   CPU1   CPU2] - CTRace.MCDS.TCX.ProgramTrace [RELative   ABSolute   NESTing   NONE] - CTRace.MCDS.TCX.ReadAddr [ON   OFF] - CTRace.MCDS.TCX.WriteAddr [ON   OFF] - CTRace.MCDS.TCX.WriteData [ON   OFF] - CTRace.MCDS.TCY.CORE [NONE   CPU1   CPU2   OTGM] - CTRace.MCDS.TCY.ProgramTrace [RELative   ABSolute   NESTing   NONE] - CTRace.MCDS.TCY.ReadAddr [ON   OFF] - CTRace.MCDS.TCY.WriteAddr [ON   OFF] - CTRace.MCDS.TCY.WriteData [ON   OFF] - CTRace.MCDS.view - CTRace.SAVE [filename] [/<option>] - CTRace.STAT.sYmbol
		Added drawing commands - var.draw - var.addwatch
Fix	UI	Fixed the problem that did not show source codes in the standard library

**- CVD\_TriCore Ver 1.01 (2015/03) Release Note**

Division	Items	Contents
New	Common	Support new cores - TC220T / TC230T / TC260T / TC270T / TC290T

		Support Debug Port Type - JTAG / DAP2
		Support General / System registers